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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Taketo WATANABE et al.**

Group Art Unit: **2814**

Serial No.: **10/633,538**

Examiner: **Hoai V. Pham**

Filed: **August 5, 2003**

Confirmation No.: **6451**

For: **SEMICONDUCTOR DEVICE, MANUFACTURING METHOD  
THEREOF, AND CMOS INTEGRATED CIRCUIT DEVICE**

Attorney Docket Number: **030927**

Customer Number: **38834**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P. O. Box 145  
Alexandria, VA 22313-1450

Date: July 7, 2005

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated March 10, 2005 for **one month** from **June 10, 2005 to July 10, 2005**.

Attached please find a check in the amount of **\$120.00** to cover the cost of the extension.

If any additional fees are due in connection with this paper, please charge our Deposit Account No. 50-2866.

Respectfully submitted,

**WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP**

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SGA/AGM/meu

Attachment: Check \$120